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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Kazuya Sayanagi, et al.

Serial No.: 09/974,668

Filed: October 10, 2001

Date: December 18, 2001

Group Art Unit: --

Examiner: --

For: HIGH-FREQUENCY CIRCUIT BOARD UNIT, HIGH-FREQUENCY MODULE USING
THE SAME UNIT, ELECTRONIC APPARATUS USING THE SAME MODULE, AND
MANUFACTURING METHOD FOR THE HIGH-FREQUENCY CIRCUIT BOARD UNIT

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Sir:

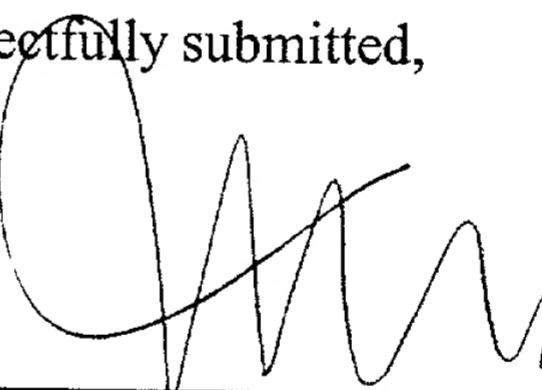
The attached Notice, issued November 14 2001, contains a requirement to file formal drawings. Attached are 6 sheets of formal drawings containing Figs. 1 through 11 for the above identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on December 18, 2001

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December 18, 2001
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Respectfully submitted,


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